Bryan Ackland is currently an Adjunct Professor in the Department of Electrical & Computer Engineering at Stevens Institute of Technology.

He received the B.Sc. in Physics from Flinders University, Australia in 1972 and his B.E. and Ph.D. in Electrical Engineering from the University of Adelaide, Australia in 1975 and 1979 respectively.

In 1978, he joined Bell Laboratories as a member of technical staff in the Image Processing and Display Research Department where he was a pioneer in full custom VLSI layout & compaction and MOS timing simulation. In 1986 he was appointed Director of the DSP & VLSI Systems Research in Holmdel, NJ where he led research in video coding VLSI, multiprocessor DSP and high speed optical transceivers. He also led a team at Bell Labs that produced the first single chip CMOS imager that had performance comparable to a CCD. In 2001, he became VP of Circuits and Systems Research at Agere Systems where he oversaw nearly 100 Ph.D. researchers working on various aspects of high performance data networking, multimedia and communication systems. In 2004 he joined NoblePeak Vision as VP Engineering where he defined the architecture and led the circuit design of their TriWave imaging chip, including a novel CDS scheme to eliminate reset and flicker noise in the pixel.

In 2011 he joined Stevens Institute of Technology as a Distinguished Service Professor and taught a number of undergraduate and graduate courses in computer architecture, digital design, electronic devices and VLSI design. He retired from full-time teaching in 2018.

Dr. Ackland has for many years been heavily involved in the IEEE Solid State Circuits Society and the Council on Electronic Design Automation. He is a member of the Executive Committee of the International Solid State Circuits Conference and has been a General Chair of the Design Automation Conference.

Dr. Ackland is the holder of 24 US Patents and the author of over 60 refereed technical publications. He is a Bell Laboratories Fellow and an IEEE Fellow.