

## Bryan Ackland – Publications

1. Åberg, I., Ackland, B., Beach, J., Godek, C., Johnson, R., King, C., Lattes, A., O'Neill, J., Pappas, S., Sriram, T. and Rafferty, C. "A Low Dark Current and High Quantum Efficiency Monolithic Germanium-on-Silicon CMOS Imager Technology for Day and Night Imaging Applications", *IEEE International Electron Device Meeting*, San Francisco CA, Dec. 2010.
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4. Rafferty, C., King, C., Ackland, B., Aberg, I., Sriram, T. and O'Neill, J. "Monolithic Germanium SWIR Imaging Array", *SPIE Defense & Security Symposium*, Orlando FL, Apr. 2008, Vol. 6940, No. 20.
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13. Ackland, B., Anesko, A., Brinthaup, D., Daubert, S., Kalavade, A., Knobloch, J., Micca, E., Moturi, M., Nicol, C., O'Neill, J., Othmer, J., Sackinger, E., Singh, K., Sweey, J., Terman, C. and Williams, J. "A Single Chip 1.6 Billion 16-b MAC/s Multiprocessor DSP", *IEEE Custom Integrated Circuits Conference*, San Diego CA, May 1999, pp. 537-540.
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**Granted Patents:**

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